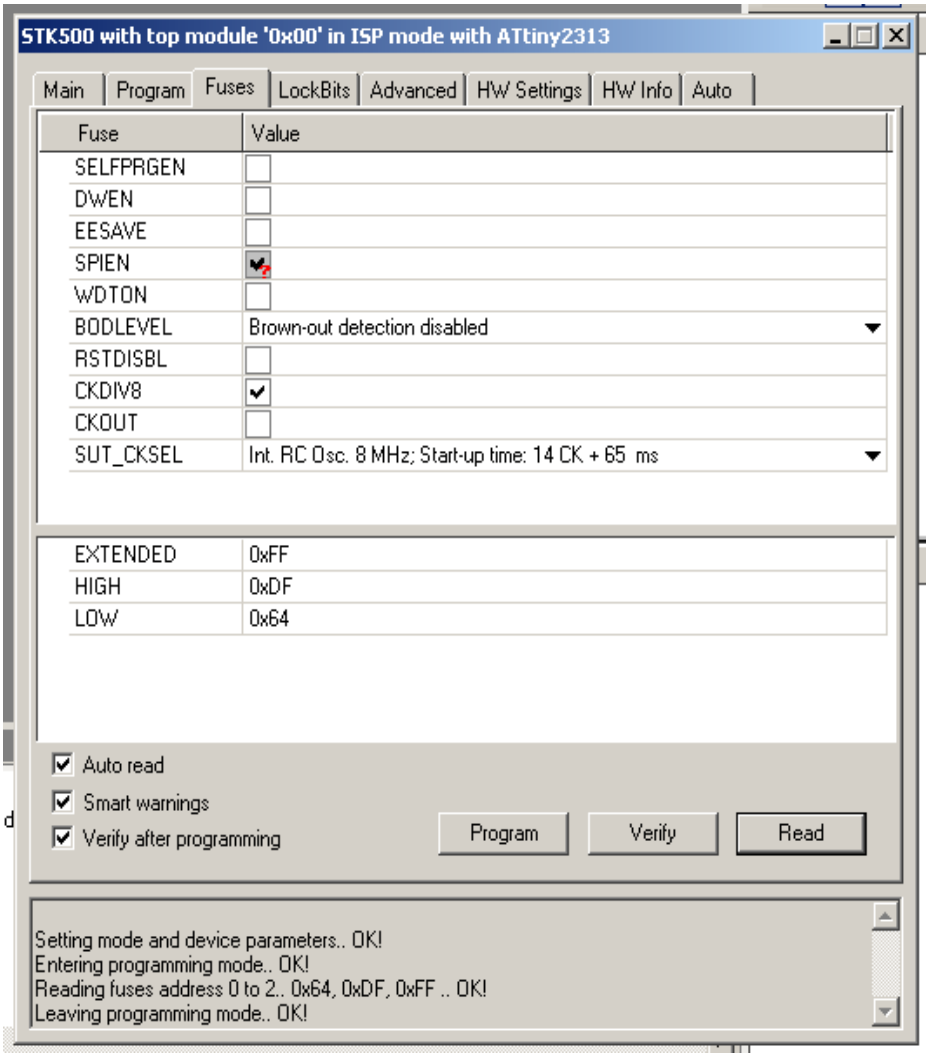
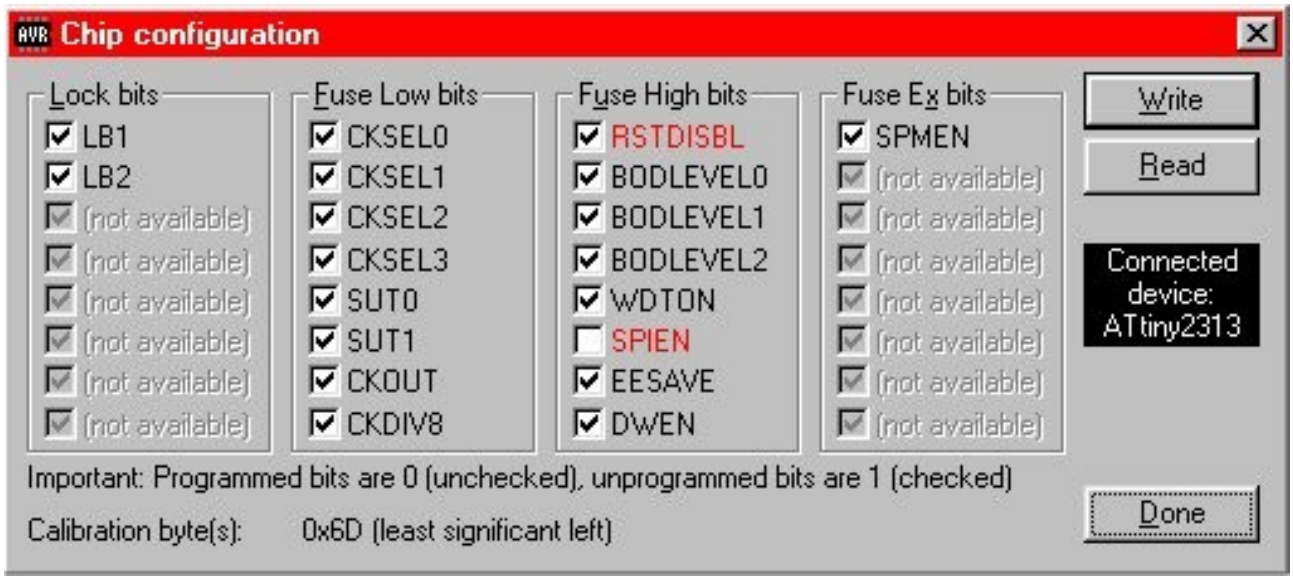

STK500 with top module '0x00' in ISP mode with ATtiny2313
Main
Program
Fuses
LockBits
Advanced
HW Settings
HW Info
Auto
Fuse
Value
SELFPRGEN
DWEN
EESAVE
SPIEN
WDTON
BODLEVEL
Brown-out detection disabled
RSTDISBL
CKDIV8
CKOUT
SUT_CKSEL
Int. RC Osc. 8 MHz; Start-up time: 14 CK + 65 ms
EXTENDED
0xFF
HIGH
0xDF
LOW
0x64
Auto read
Smart warnings
Verify after programming
Program
Verify
Read
Setting mode and device parameters.. OK!
Entering programming mode.. OK!
Reading fuses address 0 to 2.. 0x64, 0xDF, 0xFF .. OK!
Leaving programming mode.. OK!


Chip configuration
Lock bits
LB1
LB2
(not available)
(not available)
(not available)
(not available)
(not available)
(not available)
Fuse Low bits
CKSEL0
CKSEL1
CKSEL2
CKSEL3
SUT0
SUT1
CKOUT
CKDIV8
Fuse High bits
RSTDISBL
BODLEVEL0
BODLEVEL1
BODLEVEL2
WDTON
SPIEN
EESAVE
DWEN
Fuse Ex bits
SPMEN
(not available)
(not available)
(not available)
(not available)
(not available)
(not available)
(not available)
Write
Read
Connected device: ATtiny2313
Important: Programmed bits are 0 (unchecked), unprogrammed bits are 1 (checked)
Calibration byte(s): 0x6D (least significant left)
Done